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author = {Cliff Young and David S. Johnson and Michael D. Smith and David R. Karger},
title = {Near-optimal intraprocedural branch alignment},
booktitle = {Proceedings of the ACM SIGPLAN 1997 conference on Programming language},
year = {1997},
isbn = {0-89791-907-6},
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location = {Las Vegas, Nevada, United States},
doi = {http://doi.acm.org/10.1145/258915.258932},
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L Number	Hits	Search Text	DB	Time stamp
87	17	(compile\$5 with ((re\$1map\$4 re-map\$4 re\$1organiz\$3 re-organiz\$3) near2 (code data instruction)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/27 14:22
88	32	(compile\$5 same ((re\$1map\$4 re-map\$4 re\$1organiz\$3 re-organiz\$3) near2 (code data instruction)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/27 14:23
93	0	(harvard adj architecture) same (instruction adj memory) same (instruction adj cache) same (data adj memory) same (data adj cache)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/27 16:25
94	24	(harvard adj architecture) and (instruction adj memory) and (instruction adj cache) and (data adj memory) and (data adj cache)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/27 16:25
	48	((5918246") or ("4755935") or ("5717883") or ("5812810") or ("4471433") or ("5774710") or ("5956478") or ("6055640") or ("6219759") or ("6219759") or ("6282707") or ("6301652") or ("6324686") or ("5515521") or ("5613118") or ("6014740") or ("6247122") or ("6321331") or ("6349377") or ("6421774") or ("5321698") or ("5778434") or ("6173369") or ("5829024") or ("4757445") or ("4794523") or ("5412786") or ("5692119") or ("5728961") or ("6018759") or ("6219773") or ("6219773") or ("4811206") or ("4914582") or ("4928239") or ("5353419") or ("5465360") or ("5513353") or ("5546348") or ("5568632") or ("5606529") or ("5611078") or ("5664147") or ("5666516") or ("5737565") or ("5737604") or ("5740399") or ("5758119") or ("5761698") or ("5765197")).PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 10:45
	49	((5790838") or ("5870602") or ("5943687") or ("5978929") or ("6003115") or ("6059835") or ("6059835") or ("6085291") or ("6122716") or ("6173358") or ("4583165") or ("4589065") or ("4594660") or ("4604691") or ("4755937") or ("4970641") or ("4980823") or ("5185694") or ("5226138") or ("5293586") or ("5301295") or ("5305280") or ("5359713") or ("5371872") or ("5404480") or ("5455924") or ("5459842") or ("5471624") or ("5479634") or ("5493667") or ("5519853") or ("5548735") or ("5557737") or ("5615375") or ("5623628") or ("5632026") or ("5651136") or ("5652852") or ("5682522") or ("5684998") or ("5694568") or ("5696917") or ("5708792") or ("5737579") or ("5752261") or ("5761468") or ("5801775") or ("5870625") or ("5893142") or ("5918045")).PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 11:57
	86	711/125.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:01
	69	711/125.cor. and @ad<20010114	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:01
	42	cache\$1less cache-less	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:46
	8	(pre\$1initializ\$5 pre-initializ\$5) near2 memory	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:49
	1	(pre\$1initializ\$5 pre-initializ\$5) near2 instruction	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:49

	38	(pre\$1load\$3 pre-load\$3) near2 (instruction and memory)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:57
	168	(pre\$1load\$3 pre-load\$3) near static\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:58
	98	(pre\$1load\$3 pre-load\$3) near partial\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:59
	83	((pre\$1load\$3 pre-load\$3) near partial\$4) and @ad<20010114	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/24 12:59
	99	711/213.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 09:25
	496	713/2.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 09:26
	144	712/207.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 09:26
	287	712/237-239.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 09:26
	1026	711/213.cor. 713/2.cor. 712/207.cor. 712/237-239.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 09:26
	835	(711/213.cor. 713/2.cor. 712/207.cor. 712/237-239.cor.) and @ad<20010123	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 09:27
	165	711/137.cor. and @ad<20010114	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:38
	251	(711/137.cor. and @ad<20010114) 711/125.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:37
	247	711/137.cor.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:37
	25	branch near2 (pre\$load\$3 pre-load\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:47
	7	jump near2 (pre\$load\$3 pre-load\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:47
	0	(jump branch) near2 (pre\$initializ\$3 pre-initializ\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:50

	3	(jump branch) same (pre\$initializ\$3 pre-initializ\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:48
	0	(jump branch) near2 (pre\$cach\$3 pre-cach\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:50
	5	(jump branch) same (pre\$cach\$3 pre-cach\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:51
	106	deterministic with cache	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 11:52
	58	(deterministic with cache) and @ad<20010123	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 12:20
	158	(real adj time) near2 cache	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 12:36
	93	((real adj time) near2 cache) and @ad<20010123	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 12:21
	15	(real adj time) near2 (instruction and buffer)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 14:41
	277	instruction near (preload\$3 pre-load\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 14:42
	224	(instruction near (preload\$3 pre-load\$3)) and @ad<20010123	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 15:22
	7	prefix adj caching	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 15:34
	55	prefix adj cache	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 15:34
	51	(prefix adj cache) not (prefix adj caching)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 15:36
	25	(preload\$3 pre-load\$3) near2 (instruction near2 memory)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 16:07
	0	CPU same controller same main same memory same cache same address same data same control same bus same "Fig."	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 16:22
	230	CPU same controller same main same memory same cache same address same data same control same bus	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 16:24

-	106	CPU same (memory adj controller) same (main adj memory) same cache same address same data same control same bus	USPAT; US-PGPUB; EPO; JPO; IBM_TDB USPAT;	2004/02/25 16:24
-	100	(CPU same (memory adj controller) same (main adj memory) same cache same address same data same control same bus) and @ad<20010123	US-PGPUB; EPO; JPO; IBM_TDB USPAT;	2004/02/25 16:40
-	447	711/118.cor.	US-PGPUB; EPO; JPO; IBM_TDB USPAT;	2004/02/25 16:40
-	305	711/118.cor. and @ad<20010123	US-PGPUB; EPO; JPO; IBM_TDB USPAT;	2004/02/25 16:42
-	32	(711/118.cor. and @ad<20010123) and (instruction near2 buffer\$3)	US-PGPUB; EPO; JPO; IBM_TDB	2004/02/25 16:42